

Highest Resolution Charge Coupled Devices (CCDs) – Overview

Part No.	Resolution Pixels	Pixel Size in μm	No. of outputs	Total Image Area in mm	Remarks
CCD0700A	2k x 2k	15	2	30 x 60	2 improved low noise outputs, 4 devices per wafer; custom device
CCD0820A	2k x 2k	15	2	28,6 x 28,6	2 low noise outputs, 2 high speed outputs, MPP-Mode
CCD1400A	2k x 2k	27	4	61 x 61	4 low noise outputs, MPP-Mode, 2 side buttable, 2 devices per wafer, custom device
CCD2520A	4k x 3k	8,6	32		
CCD2900A	4k x 4k	15	4		
CCD1000A	4k x 4k	12		64 cells of 5,8x5,8 image area	Orthogonal Transfer Array (OTA), NMOS-logic, 3 side buttable, 3 devices per wafer, custom device
CCD1700A	4k x 4k	10	4 oder 32	40 x 40	4 or 32 low noise outputs, 2 side buttable, 4 devices per wafer, custom device
CCD1100A	4k x 8k	8	16	32,3 x 65	16 outputs at 30 MHz, frame store option, 3 devices per wafer
CCD1800A	4k oder 8k x 54-164	8 oder 16	36	69 x 19	36 high speed outputs with 20 MHz readout capability, Resolution varies by color band
CCD2500A	8k x 6k	8,6	32		
CCD2700A	8k x 8k	7,5	8		
CCD1600A	10,6 x 10,6	9	16	95 x 95	16 low noise outputs, MPP-Mode available, 2 side buttable, 1 device per wafer

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